IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Paul M. Henry et al.

Examiner:

Terry Lee Englund

Serial No .:

09/848,625

Group Art Unit:

2816

Filed:

May 3, 2001

Docket:

50019.51US01/PO4881

Confirmation No.:

7855

Due Date:

October 18, 2002

Title:

TRANSIENT REJECTING MEMORY CIRCUIT

ERTIFICATE UNDER 37 CFR 1.8:

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Box Non-Fee Amendment, Commissioner for Patents, Washington, D.C.

20231 on October 18, 2002.

Name: Tim P. Sulliva

Non-Fee Amendment Commissioner for Patents Washington, D.C. 20231

Sir:

OCT 2 3 2002

We are transmitting herewith the attached:

☐ Transmittal Sheet in duplicate containing Certificate of Mailing

Amendment Including Version With Markings To Show Changes Made including a clean copy of FIGURE 9, and a marked version of FIGURE 9 Showing the corrections

Return postcard

Please consider this a PETITION FOR EXTENSION OF TIME for a sufficient number of months to enter these papers or any future reply, if appropriate. Please charge any additional fees or credit overpayment to Deposit Account No. 13-2725. A duplicate of this sheet is enclosed.

MERCHANT & GOULD P.C. P.O. Box 2903, Minneapolis, MN 55402-0903 206.342.6200

PATENT TRADEMARK OFFICE

By: Name: Timothy P. Sullivar

Reg. No.: 47,981

Timothy P. Sullivan/sml

S/N 09/865,162

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50019.51US01/P04881

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited in the United States Postal Service, as first class mail, with sufficient postage, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231 on October 18, 2003.

Name: Timethy P. Sullivan

AMENDMENT AND REQUEST FOR RECONSIDERATION

Box Non-Fee Amendment Commissioner for Patents Washington, D.C. 20231

Dear Sir:

In response to the Examiner's Office Action mailed July 18, 2002, applicant respectfully requests a reconsideration and advancement of the application as amended.

AMENDMENT

In the Specification:

Please substitute the following for the paragraph on page 9, lines 22-26 with the following:

--It will be appreciated in view of the present disclosure that transistors M101, M102, M103, and M104 may be NPN transistors, PNP transistors, Bipolar devices, MOS devices, GaAsFET devices, JFET devices, as well as one or more components that are arranged to provide the function of transistors M101, M102, M103, and M104 in the above described example.--

In the Claims:

RECEIVED